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APPLICANTS

Akira Yoshida, Chiba, JAPAN;
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** CONTINUING DATA ***** *None eh*

** FOREIGN APPLICATIONS *****
 JAPAN 2001-071381 03/14/2001 *yes eh*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 03/05/2002

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 11	TOTAL CLAIMS 11	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after allowance	Examiner's Signature <i>Eliseh Chang</i>	Initials <i>eh</i>		

ADDRESS

Venable
 P.O. Box 34385
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TITLE

Signal compensation circuit and demodulating circuit with high-speed and low-speed feedback loops

FILING FEE RECEIVED 740	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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